



2827

PATENT APPLICATION

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of

Docket No: Q56857

Yasuyoshi YAMADA

Appln. No.: 09/435,448

Group Art Unit: 2827

Confirmation No.: 5236

Examiner: Graybill, David E.

Filed: November 22, 1999

For: BACK ELECTRODE TYPE ELECTRONIC PART AND ELECTRONIC ASSEMBLY
WITH THE SAME MOUNTED ON PRINTED CIRCUIT BOARD

**INFORMATION DISCLOSURE STATEMENT
UNDER 37 C.F.R. §§ 1.97 and 1.98**

Commissioner for Patents
Washington, D.C. 20231

Sir:

In accordance with the duty of disclosure under 37 C.F.R. § 1.56, Applicant hereby notifies the U.S. Patent and Trademark Office of the document which is listed on the attached PTO/SB/08 A & B (modified) form and listed herein and which the Examiner may deem material to patentability of the claims of the above-identified application.

1. Japanese Unexamined Patent Publication No. H11-074637, published March 16, 1999.

One copy of the listed document is submitted herewith.

The present Information Disclosure Statement is being filed after the later of three months from the application's filing date and the mailing date of the first Office Action on the merits, but before a Final Office Action, Notice of Allowance, or an action that otherwise closes prosecution in the application (whichever is earlier), and therefore Applicant is filing

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INFORMATION DISCLOSURE STATEMENT
U.S. Appln. No.: 09/435,448

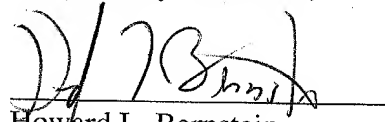
concurrently herewith a Statement Under 37 C.F.R. § 1.97(e). No fee under 37 C.F.R. § 1.17(p) is required.

The present Information Disclosure Statement is being filed thirty days or fewer from the communication from a foreign patent office and a Statement Under 37 C.F.R. § 1.704(d) is attached.

In compliance with the concise explanation requirement under 37 C.F.R. § 1.98(a)(3) for foreign language documents, Applicant encloses here with a copy of a corresponding Japanese Office Action dated January 21, 2003 and an English translation of the pertinent portions thereof which cites such documents and indicates the degree of relevance found by the foreign office.

The submission of the listed documents is not intended as an admission that any such document constitutes prior art against the claims of the present application. Applicant does not waive any right to take any action that would be appropriate to antedate or otherwise remove any listed document as a competent reference against the claims of the present application.

Respectfully submitted,


Howard L. Bernstein
Registration No. 25,665

SUGHRUE MION, PLLC
Telephone: (202) 293-7060
Facsimile: (202) 293-7860

WASHINGTON OFFICE



23373

PATENT TRADEMARK OFFICE

Date: February 20, 2003



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STATEMENT UNDER 37 C.F.R. § 1.704(d)

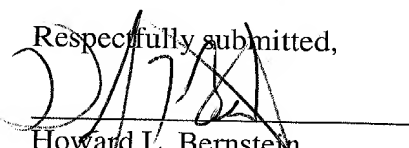
Commissioner for Patents
Washington, D.C. 20231

Sir:

The undersigned hereby states, upon information and belief:

That each item of information contained in the Information Disclosure Statement filed concurrently herewith was cited in a communication from a foreign patent office in a counterpart foreign application, and, to the knowledge of the undersigned, that the communication was not received by any individual designated in 37 C.F.R. § 1.56(c) more than thirty days prior to the filing of said Information Disclosure Statement.

Respectfully submitted,


Howard L. Bernstein
Registration No. 25,665

SUGHRUE MION, PLLC
Telephone: (202) 293-7060
Facsimile: (202) 293-7860

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of

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Confirmation Number	5236
Filing Date	November 22, 1999
First Named Inventor	Yasuyoshi YAMADA
Art Unit	2827
Examiner Name	Graybill, David E.
Attorney Docket Number	Q56857

[illegible][illegible][illegible]**Date Considered**

¹Applicant's unique citation designation number (optional). ²See Kind Codes of USPTO Patent Documents at www.uspto.gov, MPEP 901.04 or in the comment box of this document. ³Enter Office that issued the document, by the two-letter code (WIPO Standard ST. 3). ⁴For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document. ⁵Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST. 16 if possible. ⁶Applicant is to indicate here if English language Translation is attached.

Ref. Q56857

Reasons

The inventions pertaining to the claims listed below in the present application are the same as inventions described in the specification or drawings attached initially to the application form of the patent application listed below, for which the application was laid open after the application was made, in a patent application that was submitted prior to the date of the [present] patent application, and, additionally, the inventors in the present application are not the same as the individuals that created the aforementioned inventions pertaining to the patent application that is earlier than the present application, nor, at the time that the application was made, was the patent applicant the same as the patent applicant in the aforementioned patent application, and thus [said inventions] are ineligible to receive patent protection under the stipulations of Article 29, Part 2 of the Patent Law.

Annotation (See the List of Cited Literature, etc. for the cited literature, etc.)

* Claims: 1 to 22

* Cited Literature, etc.: 1

* Comments:

The specification in Cited Literature, etc., 1 describes an electrical component device that is a printed wiring board whereon square back-surface electrode-type electrical components equipped with multiple BGA electrode terminals are mounted, which is an electric component device that is a combination of a printed wiring board equipped with integrated lands connected together and multiple BGA electrode terminals that exist on the corner parts of the back surface electrode-type electrical components within the BGA electrode terminals, combined with square back surface electrode-type electrical components and a printed wiring board, wherein said back surface electrode-type electrical components are equipped with multiple BGA electrode terminals, and wherein the printed wiring boards are equipped with integrated lands connected together and with multiple BGA electrode terminals on the corners of the back surface electrode-type electrical components within the BGA electrode terminals.

Whether the multiple BGA electrode terminals are ground electrode terminals, non-connected electrode terminals, terminals with the same signal, or power supply terminals is seen as nothing but a trivial difference in design.

From this perspective, the inventions according to Claims 1 to 22 in this case are essentially the same as the invention described in the specification in Cited Reference, Etc. 1.

List of Cited Literature, etc.

1. Japanese Unexamined Patent Application Publication H09-234213 (Japanese Unexamined Patent Application Publication H11-074637)